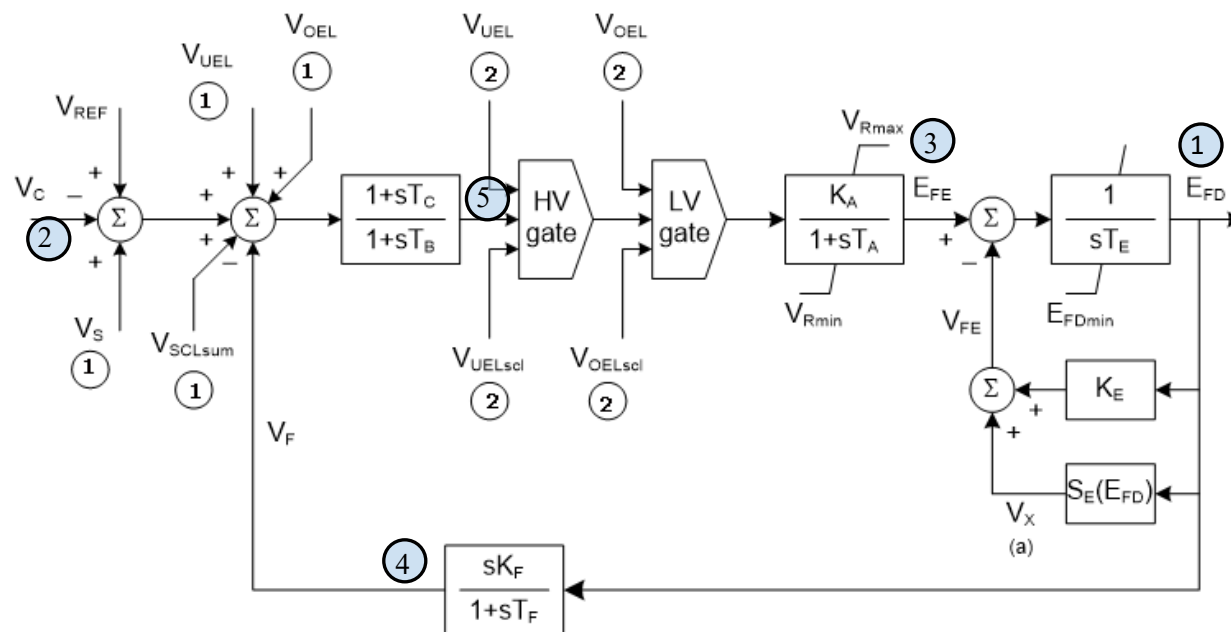


Exciter DC1C

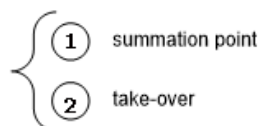
Exciter DC1C IEEE Std 421.5-2016 DC Commutator Excitation System Model



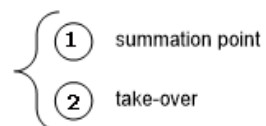
States:

- 1 – E_{FD}
- 2 – Sensed V_T
- 3 – V_R or E_{FE}
- 4 – V_F
- 5 – Lead-Lag

alternate
OEL input
locations
(V_{OEL})



alternate
UEL input
locations
(V_{UEL})



alternate
SCL input
locations
(V_{SCL})

